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## TRANSMITTAL FORM

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Total Number of Pages in This Submission

15

Application Number	10/616,850
Filing Date	7/10/2003
First Named Inventor	DHONG
Art Unit	2819
Examiner Name	Vibol TAN
Attorney Docket Number	AUS920020711US1 (IBM 2601000)

ENCLOSURES (Check all that apply)			
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<p><b>Remarks</b> Reply to Office Action dated November 8, 2004 (14 pages).</p>			

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	CARR LLP		
Signature			
Printed name	Theodore F. Shiells		
Date	2/7/05	Reg. No.	31,569

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PATENT APPLICATION  
SERIAL NO. 10/616,850

DSW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Dhong et al.

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Serial Number: 10/616,850

Group Art Unit: 2819

Filed: July 10, 2003

Examiner: Tan, Vibol

For: INTEGRATED LOGIC AND LATCH  
DESIGNS WITH CLOCK GATING AT  
STATIC INPUT SIGNALS

Mail Stop Amendment  
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on:	<u>2/7/05</u>
<u>Bradley D. Ellis</u> Bradley D. Ellis	

RESPONSE

Dear Sir:

In response to the Office Action dated November 8, 2004, ("Office Action"), Applicants respond as follows: